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APPLICATION NO.	. F	ILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO	
09/891,727		06/26/2001	Daniel M. Kinzer	IR-1698 (2-2027)	IR-1698 (2-2027) 1370	
2352	7590	06/26/2003				
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1180 AVE NEW YOR		HE AMERICAS 00368403		LEWIS, MONICA		
				ART UNIT	PAPER NUMBER	
				2822		
			DATE MAILED: 06/26/2003			

Please find below and/or attached an Office communication concerning this application or proceeding.

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		Application No. Applicant(s)					
1 دسم	Advisory Action	09/891,727	KINZER ET AL.				
	·	Examiner	Art Unit				
		Monica Lewis	2822				
	The MAILING DATE of this communication appe						
Theret final re conditi	REPLY FILED 29 May 2003 FAILS TO PLACE TH fore, further action by the applicant is required to a ejection under 37 CFR 1.113 may only be either: (for for allowance; (2) a timely filed Notice of Appendation (RCE) in compliance with 37 CFR 1.114.	void abandonment of this appli 1) a timely filed amendment wh	cation. A proper reply t ich places the application	to a on in			
	PERIOD FOR RE	EPLY [check either a) or b)]					
ь) [event, however, will the statutory period for reply expire later th ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS 706.07(f).	risory Action, or (2) the date set forth in the an SIX MONTHS from the mailing date of FILED WITHIN TWO MONTHS OF THE	of the final rejection. IE FINAL REJECTION. See N	MPEP			
have be 37 CFR (b) abov	ensions of time may be obtained under 37 CFR 1.136(a). The date filed is the date for purposes of determining the period of exten 1.17(a) is calculated from: (1) the expiration date of the shortened e, if checked. Any reply received by the Office later than three monatent term adjustment. See 37 CFR 1.704(b).	sion and the corresponding amount of the distatutory period for reply originally set in	e fee. The appropriate extensi the final Office action; or (2) a	ion fee under as set forth in			
1.	A Notice of Appeal was filed on Appellant' 37 CFR 1.192(a), or any extension thereof (37 CF	s Brief must be filed within the R 1.191(d)), to avoid dismissal	period set forth in of the appeal.				
2.	The proposed amendment(s) will not be entered b	ecause:					
(a) They raise new issues that would require further consideration and/or search (see NOTE below);							
(b) ☐ they raise the issue of new matter (see Note below);							
(c)	they are not deemed to place the application issues for appeal; and/or						
(d)	they present additional claims without cance NOTE:	ling a corresponding number of	finally rejected claims.				
3.	Applicant's reply has overcome the following reject	ction(s):					
4.	Newly proposed or amended claim(s) would canceling the non-allowable claim(s).	be allowable if submitted in a	separate, timely filed a	mendment			
5.🖂	The a) \square affidavit, b) \square exhibit, or c) \boxtimes request for application in condition for allowance because: \underline{Se}		nsidered but does NOT	place the			
6.	The affidavit or exhibit will NOT be considered be raised by the Examiner in the final rejection.	cause it is not directed SOLEL'	Y to issues which were	newly			
7.🛛	For purposes of Appeal, the proposed amendmen explanation of how the new or amended claims w	t(s) a)□ will not be entered or ould be rejected is provided be	b)⊠ will be entered and elow or appended.	d an			
	The status of the claim(s) is (or will be) as follows	:					
	Claim(s) allowed:						
•	Claim(s) objected to:						
	Claim(s) rejected: <u>1-28</u> .						
	Claim(s) withdrawn from consideration:						
8.	The proposed drawing correction filed on is	s a)□ approved or b)□ disap	oproved by the Examine	er.			
9.	Note the attached Information Disclosure Stateme	ent(s)(PTO-1449) Paper No(s).	·				
10.	10. Other:						
		AMIR	ZARABIAN				
		SUPERVISORY TECHNOLOG	PATENT EXAMINER BY CENTER 2800				

Continuation of 5. does NOT place the application in condition for allowance because: Applicant argues that Ranjan does not disclose "to cause each other to deplete under blocking voltage conditions." However, Ranjan does disclose blocking voltage conditions (For Example: See Column 1 Lines 16-21). Additionally, Applicant argues that Ranjan does not disclose that "smaller lateral extent can be achieved to reduce the chip area taken by the device." However, Ranjan does disclose that a smaller lateral extent can be achieved (Fo Example: See Column 2 Lines 7-16). Finally, Applicant argues that Uenishi does not disclose a mesa between the drain and MOSGate structure. However, Uenishi discloses a mesa as defined by Applicant in claim 1 (For Example: See Figure 1) and the motivation to combine in order to provide a high breakdown voltage is disclosed (For Example: See Abstract and Column 4 lines 45-50).